FORM PTO-1449		DOCKET NUMBER APPLICATION NUMBER						
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				Hirohiko Nichiki				
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U.S. PATENT DOCUMENTS								
EXAMINER	DOCUMENT	DATE		NAME	CLA	ASS	SUB	FIL. DATE
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A-5.	5,699,139	12/16/97	Aastue	349		156	7/5/95	
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OTHER DOCUMENTS								
article entitled, "Tri-layer a-Si:H Integrated Circuits on Polymeric Substrates" by								
	article entitled, "Tri-layer a-Si:H Integrated Circuits on Polymeric Substrates", by Thomasson et al., IEDM 98, pp. 253-256.							
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